Embedded System Design Techniques™

Bootloader Design for MCUs

Session 4: Bootloader Implementation

January 28th, 2016 Jacob Beningo, CSDP



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Course Overview

- Bootloader Models and Concepts
- Protocol Design

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- Setting up a Test Application
- Bootloader Implementation
- Troubleshooting Techniques





Session Overview

- Bootloader System Review
- Start-up Branching
- Startup Checks

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- Assembling the Image
- Resetting the Image



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Bootloader System Review



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Advantages	Disadvantages
Code can be in assembly	Susceptibility to start-up noise
Branch is executed quickly	Dedicated GPIO
Very simple implementation	Accidental bootloader entry

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Example:

Assembly branch code

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jmp main ;	Procented b
lds #StackTop	
GoBoot:	
jmp 0,x	; jump to the application
ldx AppResetVect	
Idd AppResetVect	; Load the Application Reset Vector
	; if PPO == 1 then start the application
brclr \$0259, \$01, GoBoot	; if PP0 == 0 then start the boot-loader

What is the potential flaw with the previous branch code?



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Example:

Checking the reset vector

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jmp main	; Continue Boot-loader startup
lds #StackTop	
_GoBoot:	
jmp 0,x	; jump to the application
ldx AppResetVect	
	; available then start the bootloader
beq _GoBoot	; if the application reset vector is not
cpd #\$ffff	; Compare it to 0xFFFF
ldd AppResetVect	; Load the Application Reset Vector
	; if PPO == 1 then start the application
brclr \$0259, \$01, GoBoot	; if PP0 == 0 then start the boot-loader

• Example "Advanced" Check

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jmp main	; Continue Boot-loader startup
lds #StackTop	
_GoBoot:	
Jmp U,x	; jump to the application
ldx AppResetVect	
	; boot-loader, otherwise continue to the application
beq _GoBoot	; if Status == 'B' for Boot-loader then jump to
cpd #'B'	; Compare it to 'B' for boot-load
Idd EepromProgStatu	s ; Read the programmed status byte from eeprom
	; available then start the bootloader
beq _GoBoot	; if the application reset vector is not
cpd #\$ffff	; Compare it to 0xFFFF
Idd AppResetVect	; Load the Application Reset Vector

bv:

• Integrating the branch code and the bootloader



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Startup Checks



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Startup Checks

• Branch Code in C

```
if((Checksum Complete == TRUE) && (StartUpTmr == EXPIRED))
  ł
                                       /* Does app reset vector exist? */
    if((*ResetVector != 0xFFFF)
                                &&
      (Status != 'B')
                                 &&
                                       /* EEPROM status set? */
      (Boot ToolPresent != TRUE) && /* Tool present? */
      (Checksum_Valid != FALSE)) /* Checksum valid? */
      App_LoadImage();
    }
    else
      Boot_LoadImage();
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```

Assembling the Image

Requirements

- Command driven vs image driven
- Commands
 - Lock/Unlock Flash
 - Read/Write Configuration
 - Image/Record Data
 - Switch to Application
- Image Driven
 - Continuously loops through image
 - Completely Autonomous





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Assembling the Image

- A block of image data is usually larger than can be directly communicated
- Memory region broken up into separate packets
- Packets need to be reassembled and validity checked
- Steps
 - Receive image packets
 - Reassemble into image block
 - Verify Checksum
 - Write
 - Acknowledge
- Repeat until completed

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Assembling the Image



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Resetting the System

- How to reset the system
 - Watchdog timer
 - Infinite loop
 - Illegal write to register
 - Soft reset command
 - Manual software reset
 - Notify user to power cycle



```
void Wdt_Reset(void)
{
    /* Enter an invalid key to force reset */
    SWT.SR.R = 0x0000FFFF;
}
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```

Additional Resources

- Download Course Material for
 - Updated C Doxygen Templates (Sept 2015)
 - Example source code
 - Bootloader White Paper
 - Templates
- Microcontroller API Standard
- EDN Embedded Basics Articles
- Embedded Bytes Newsletter



From <u>www.beningo.com</u> under

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