



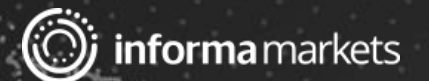
**DesignNews**

Introduction to Build Systems and CMake

# DAY 3 : CMake for Embedded Systems

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01

# Review: The Problem

# The Problem

There are several problems that teams are facing:

- Managing multiple build configurations
- Slow builds
- Software quality issues
- Inability to use modern techniques like DevOps, Simulation, TDD, etc, effectively
- Productivity issues (time to market, product quality)

## The Solution

A carefully designed CMake build system will:

- Simplify build configurations with better dependency management
- Allow for faster, cross-platform builds
- Enable consistency across different development environments
- Unlock modern development processes and tools like DevOps, Simulation, and TDD
- Increase productivity

## THE SPEAKER



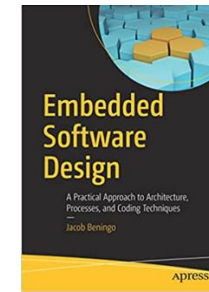
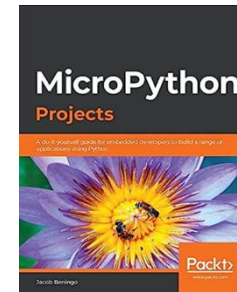
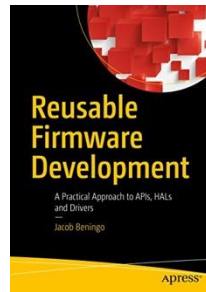
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# The Plan

**Transform Your Build Process: Streamline, Modernize, and Boost Productivity with CMake**

Step 1  
Learn the Technology

Step 2  
Design the Solution

Step 3  
Adopt Modern Practices



02

# Toolchain Files



## Toolchain Files - Introduction

A **CMake toolchain file** is a script used by CMake to define the compilation environment, particularly for cross-compilation scenarios. It allows you to

- specify the compiler
- linker,
- and various other tools and flags

that CMake should use when generating build files. Toolchain files are essential when you are building software for a different platform than the one you are working on, such as when targeting an embedded system from a desktop environment.

## Toolchain Files – The Structure

A typical CMake toolchain file is a plain text file with the `.cmake` extension, and it contains a series of commands that configure the necessary tools and flags

```
1 # Set the target system name, e.g., 'Linux', 'Windows', 'Generic', etc.
2 set(CMAKE_SYSTEM_NAME Generic)
3
4 # Specify the cross compiler to use
5 set(CMAKE_C_COMPILER /path/to/your/compiler/arm-none-eabi-gcc)
6 set(CMAKE_CXX_COMPILER /path/to/your/compiler/arm-none-eabi-g++)
7
8 # Set the tool for archiving libraries
9 set(CMAKE_AR /path/to/your/compiler/arm-none-eabi-ar)
10
11 # Set the tool for linking binaries
12 set(CMAKE_LINKER /path/to/your/compiler/arm-none-eabi-ld)
13
14 # Set the tool for the assembler
15 set(CMAKE_ASM_COMPILER /path/to/your/compiler/arm-none-eabi-as)
16
17 # Define any required compiler flags, such as for the CPU architecture
18 set(CMAKE_C_FLAGS "-mcpu=cortex-m4 -mthumb")
19 set(CMAKE_CXX_FLAGS "-mcpu=cortex-m4 -mthumb")
20
```

# Toolchain Files - Compilation

Must specify in our command!

```
cmake -DCMAKE_TOOLCHAIN_FILE=$TOOLCHAIN_FILE -G Ninja -B $BUILD_DIR -S . -DCMAKE_BUILD_TYPE=$BUILD_TYPE  
ninja -C $BUILD_DIR
```

## Audience POLL Question

What is a toolchain file used for?

- a) To manage source code versioning in a project
- b) To define the compilation environment, particularly for cross-compiling to a different platform
- c) To automate the testing of code during the build process
- d) To configure the user interface settings in a development environment



# Host Toolchains

03

## Host Toolchain Files – What are they for?

A **Host Toolchain file** is a configuration file that defines the tools, compilers, and libraries used when building software on the host machine (the machine where the build is happening).

It sets up the environment to ensure consistent builds across different machines by specifying which compiler, linker, and other tools should be used.

## Host Toolchain Files – Why do we need them?

- Embedded projects often require specific versions of compilers, linkers, and other tools that might not be the default on every developer's machine. A host toolchain file ensures these requirements are met consistently.
- **For Example:** Compiling an RTOS

```
config
├─ $ jlink-error-checks.sh
├─ ≡ program.jlink
├─ ≡ STM32L4x5.svd
├─ ≡ toolchain-arm.cmake
├─ ≡ toolchain-linux.cmake U
├─ ≡ toolchain-mac.cmake U
├─ ≡ toolchain-windows.cmake U
```

# Host Toolchain Files – Threading Example

```
# Set the system name (e.g., ARM Cortex-M)
set(CMAKE_SYSTEM_NAME Generic)
set(CMAKE_SYSTEM_PROCESSOR arm)

# Set the cross compiler
set(CMAKE_C_COMPILER arm-none-eabi-gcc)
set(CMAKE_CXX_COMPILER arm-none-eabi-g++)

# Specify the path to FreeRTOS source
set(FREERTOS_PATH /path/to/freertos/source)

# Add compiler flags specific to the embedded platform
set(CMAKE_C_FLAGS "-mcpu=cortex-m4 -mthumb -O2 -ffreestanding -fno-builtin")
set(CMAKE_CXX_FLAGS "-mcpu=cortex-m4 -mthumb -O2 -ffreestanding -fno-builtin")

# Include FreeRTOS in the build
include_directories(${FREERTOS_PATH}/include)
```

```
# Set the system name (Linux)
set(CMAKE_SYSTEM_NAME Linux)
set(CMAKE_SYSTEM_PROCESSOR x86_64)

# Set the compiler to GCC
set(CMAKE_C_COMPILER /usr/bin/gcc)
set(CMAKE_CXX_COMPILER /usr/bin/g++)

# Specify the path to FreeRTOS source for Linux
set(FREERTOS_PATH /path/to/freertos/source)

# Add necessary flags for building on Linux
set(CMAKE_C_FLAGS "-O2 -Wall")
set(CMAKE_CXX_FLAGS "-O2 -Wall")

# Include FreeRTOS in the build
include_directories(${FREERTOS_PATH}/include)

# Link with the pthread library (often required for RTOS-like
behavior on Linux)
set(CMAKE_EXE_LINKER_FLAGS "-lpthread")
```

```
# Set the system name (Windows)
set(CMAKE_SYSTEM_NAME Windows)
set(CMAKE_SYSTEM_PROCESSOR x86_64)

# Set the compiler to MinGW GCC
set(CMAKE_C_COMPILER C:/mingw/bin/gcc.exe)
set(CMAKE_CXX_COMPILER C:/mingw/bin/g++.exe)

# Specify the path to FreeRTOS source for Windows
set(FREERTOS_PATH C:/path/to/freertos/source)

# Add necessary flags for building on Windows
set(CMAKE_C_FLAGS "-O2 -Wall")
set(CMAKE_CXX_FLAGS "-O2 -Wall")

# Include FreeRTOS in the build
include_directories(${FREERTOS_PATH}/include)

# Link with the Windows threading library (if needed)
set(CMAKE_EXE_LINKER_FLAGS "-lwinpthread")
```



## Audience POLL Question

What is the most important reason to use a host toolchain file in embedded software development?

- a) To ensure consistent build environments across different development machines
- b) To simplify cross-compilation for multiple target platforms
- c) To automate the inclusion of third-party libraries and dependencies
- d) To optimize build times by using custom compiler and linker settings



# Target Toolchain Files

04

## Target Toolchain Files – What are they for?

A **Target Toolchain file** is a configuration file that defines the tools, compilers, and libraries used when cross-compiling software on a host machine (the machine where the build is happening) for a different target.

It sets up the environment to ensure consistent build for the target architecture.

## Target Toolchain Files – An Example

```
1  # By setting CMAKE_SYSTEM_NAME to "Generic," you indicate to CMake that it should avoid platform-specific
2  # configurations and try to generate a more generic build system that can be used across different
3  # environments. This is often done in cross-compilation scenarios or when building code that is intended to
4  # be platform-independent.
5  set(CMAKE_SYSTEM_NAME Generic)
6
7  # Set the toolchain base path
8  set(TOOLCHAIN_BASE_PATH "/home/dev/arm-gnu-toolchain-13.2.Rel1-x86_64-arm-none-eabi/bin/")
9
10 # Specify the cross compiler and associated tools
11 set(CMAKE_C_COMPILER "${TOOLCHAIN_BASE_PATH}arm-none-eabi-gcc")
12 set(CMAKE_CXX_COMPILER "${TOOLCHAIN_BASE_PATH}arm-none-eabi-g++")
13 set(CMAKE_ASM_COMPILER "${TOOLCHAIN_BASE_PATH}arm-none-eabi-gcc")
14 set(OBJCOPY_PATH "${TOOLCHAIN_BASE_PATH}arm-none-eabi-objcopy")
15 set(SIZE_TOOL "${TOOLCHAIN_BASE_PATH}arm-none-eabi-size")
```

## Target Toolchain Files – An Example

```
17 # MCU specific option flags
18 # We use set to create a list of flags that we want to pass to the compiler. It is a list of strings.
19 # This is a convenient and configurable method. The flags we are passing to the compiler include:
20 #
21 # -mcpu=cortex-m4: Specifies the target CPU architecture as Cortex-M4.
22 # -mthumb: Indicates that the code should be compiled for the Thumb instruction set, which is
23 # commonly used in ARM-based microcontrollers for code size optimization. You, 3 months ago
24 # -mfpv4-sp-d16: Specifies the FPU (Floating-Point Unit) type for Cortex-M4, in this case, "fpv4-sp-d16"
25 # stands for a single-precision FPU with 16 double-precision registers.
26 # -mfloat-abi=hard: Specifies that the code should use the "hard" floating-point ABI (Application Binary Interface),
27 # which means that floating-point calculations should be performed using hardware instructions
28 # (as opposed to software emulation).
29 # --specs=nano.specs: This flag is specific to some ARM toolchains (like the GCC ARM toolchain) and is used to
30 # specify linker options for using the Nano Standard C Library, which is a minimalistic
31 # version of the C library optimized for embedded systems with limited resources.
32 set(MCU_FLAGS
33     -mcpu=cortex-m4
34     -mthumb
35     -mfpv4-sp-d16
36     -mfloat-abi=hard
37     -specs=nano.specs
38 )
```

# Target Toolchain Files – An Example

```
45 # Define additional compiler symbols (-D)
46 #
47 # USE_HAL_DRIVER:      Symbol used to tell STM32 library we are using the HAL drivers
48 # STM32L475xx:        Symbol specifying the MCU target family
49 # __FPU_PRESENT       Tell the ST libraries that the FPU is present
50 add_compile_definitions(
51     USE_HAL_DRIVER
52     STM32L475xx
53     __FPU_PRESENT=1U
54 )
```

```
58 # g:                   generate debug information
59 # O2:                  Optimize code for speed
60 # Wall:                Enables a compiler warning messages that catch common programming errors
61 # Wextra:              Enables some extra warning flags that are not enabled by -Wall
62 # Werror:              Make all warnings into errors
63 # pedantic:            Issue all the warnings demanded by strict ISO C and ISO C++
64 # Wunused-variable:   Warn whenever a local variable or non-constant static variable is unused aside from its
65 # Wuninitialized:      Warn if an automatic variable is used without first being initialized
66 # Wshadow:             Warn whenever a local variable or type declaration shadows another variable
67 # fstack-protector:    Enable stack protection checks
68 # Wconversion:         Warn for implicit conversions that may alter a value
69 # Wunused-function:    Warn whenever a static function is declared but not defined or a non-inline static function
70 # funsigned-char:      Treat character data type as unsigned instead of signed
71 # fdata-sections:      Instructs the compiler to place each global or static variable in a separate data section
72 # ffunction-sections:  Instructs the compiler to place each function in a separate code section
73 set(COMMON_FLAGS
74     -g
75     -O2
76     -Wall
77     # -Wextra
78     # -Werror
79     # -pedantic
80     -Wunused-variable
81     -Wuninitialized
82     # -Wshadow
83     -fstack-protector
84     # -Wconversion
85     -Wunused-function
86     -funsigned-char
87     -fdata-sections
88     -ffunction-sections
89 )
```

## Target Toolchain Files – An Example

```
93 # C++ flags
94 #
95 # std:           Let's use C++17
96 # fno-rtti:     Disable Run-Time Type Information. Decreases binary size and improves performance
97 # fno-exceptions: For microcontrollers, we disable exceptions due to run-time overhead and dynamic allocation
98 # lstdc++:      C++ should link our application
99 set(CPP_ONLY_FLAGS
100 |   -std=c++17
101 |   -fno-rtti
102 |   -fno-exceptions
103 |   -lstdc++
104 | )
105
106 string(REPLACE ";" " " CPP_ONLY_FLAGS "${CPP_ONLY_FLAGS}")
107
108 # C flags
109 #
110 # std:           Let's use C11
111 set(C_ONLY_FLAGS
112 |   -std=c11
113 | )
114
115 string(REPLACE ";" " " C_ONLY_FLAGS "${C_ONLY_FLAGS}")
```

## Target Toolchain Files – An Example

```
126 # Linker options
127 #
128 # -T:           Linker script
129 # -specs=nosys.specs: Linker options for using the Nano Standard C Library
130 # -Wl,-Map:     Generate a map file
131 # -Wl,--cref:   Add cross reference to the map file
132 # -Wl,--gc-sections: Remove unused sections from the final binary
133 set(TOOLCHAIN_LINKER_OPTIONS
134     -T${LDSCRIPT}
135     -specs=nosys.specs
136     -Wl,-Map=${EXE_DIR}/${TARGET}.map,--cref
137     -Wl,--gc-sections
138 )
139
140 # Libraries to link
141 #
142 # c:           The C library
143 # m:           The math library
144 # nosys:      The Nano Standard C Library
145 set(TOOLCHAIN_LIBRARIES
146     c
147     m
148     nosys
149 )
```



## Audience POLL Question

Why do you use a target toolchain file when developing embedded software?

- a) To define the specific cross-compiler and linker required for the target hardware
- b) To configure hardware-specific optimization flags for better performance
- c) To manage dependencies and libraries that are specific to the target environment
- d) To ensure compatibility with the target operating system or RTOS

●● Next Steps

05

## Embedded Build System

Transform your build system with the free Beningo Embedded Build System example:

- Docker container build system
- Makefile-based
- CMake with Ninja Example
- Compilation scripts
- Integrated tools like cpputest



<https://mailchi.mp/beningo/beningo-devops>

## Additional Resources

Please consider the resources below:

- [Jacob's Blogs](#)
- [Jacob's CEC courses](#)
- [Embedded Software Academy](#)
  
- Embedded Bytes Newsletter
  - <http://bit.ly/1BAHYXm>



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## Next Steps

✓ Introduction to Embedded Build Systems

✓ CMake Fundamentals

✓ CMake for Embedded Systems

Designing your Build System

Adopting Modern Practices



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